

Form P-440 <div style="border: 2px solid black; border-radius: 50%; padding: 10px; display: inline-block; transform: rotate(-15deg);"> <b>U.S. DEPT. OF COMMERCE</b>  <b>PATENT &amp; TRADEMARK OFFICE</b>  <b>JUN 14 2007</b>  <b>ART CITED BY APPLICANT</b>  <small>(Use several sheets if necessary)</small> </div>		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-878		SERIAL NO. 09/388,857	
APPLICANT Luan C. Tran				FILING DATE 09/01/99		GROUP 2813	
U.S. PATENT DOCUMENTS							
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
AA							
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FOREIGN PATENT DOCUMENTS							
	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
AL							
AM							
AN							
AO							
AP							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
<div style="font-size: 2em; font-family: cursive;">Ref</div>	AR		S. Wolf, <i>Silicon Processing for the VLSI Era</i> , Volume 3 - The Submicron MOSFET, ©1995 Lattice Press, pages 330, 331, 366 and 367.				
	AS						
	AT						
EXAMINER <div style="font-size: 1.5em; font-family: cursive;">M. S. Schlegel</div>			DATE CONSIDERED <div style="font-size: 1.5em; font-family: cursive;">7/29/07</div>				
<small>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							